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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit.			
Date of Deposit:	05/03/05	Name of Person Making the Deposit:	Shannon Carmo
		Signature of the Person Making the Deposit:	<i>Shannon Carmo</i>

In re Application of: **YANG, et al.**

Serial No.: **10/758,148** Examiner: **PHAM, Long**
Filed: **01/14/2004** Art Unit: **2814**
Conf. No: **4639**

For: **EFFICIENT USE OF WAFER AREA WITH DEVICE UNDER THE PAD APPROACH**

Commissioner for Patents
PO BOX 1450
Alexandria, VA. 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT TRANSMITTAL

1. Transmitted herewith is an amendment for this application

☒ Transmitted herewith is a response to a Restriction Requirement for the above identified patent application.
..... (5 sheets)
..... Transmitted herewith are sheets of substitute formal drawings.
..... Other:
.....

2. Applicant is other than a small entity

Extension of Term

3. The proceedings herein are for a patent application and the provisions of 37 C.F.R. 1.136 apply.

(a) ☐ Applicant petitions for an extension of time under 37 C.F.R. 1.136
(fees: 37 C.F.R. 1.17(a)-(d) for the total number of months checked below:)

<u>Extension</u>	<u>Fee</u>
<input type="checkbox"/> one month	\$110.00
<input type="checkbox"/> two months	\$400.00
<input type="checkbox"/> three months	\$920.00
<input type="checkbox"/> four months	\$1,960.00

Fee \$ _____

If an additional extension of time is required, please consider this a petition therefor.

(b) ☒ Applicant believes that no extension of term is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the

need for a petition for extension of time.

Fee Calculation

4. The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated as shown below:

(for other than a small entity)					
Fee Items	Claims Remaining After Amendment	Highest Number of Claims Previously Paid For	Present Extra Claims	Fee Rate	Total
Total Claims	12	- 20 =	0	x \$18.00	\$0.00
Independent Claims	2	- 3 =	0	x \$80.00	\$0.00
Multiple Dependent Claim Fee (one or more, first added by this amendment)				\$260.00	\$0.00
Total Fees					\$0.00

PAYMENT OF FEES

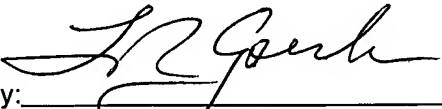
5. The full fee due in connection with this communication is provided as follows:
- ☒ [X] The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060
Customer No.: 45592

Respectfully submitted,

Date: May 3, 2005

By: 
Lawrence R. Goerke
Reg. No. 45,927



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of)	
YANG, Nian, et al.)	Examiner: PHAM, Long
Serial No. 10/758,148)	Art Unit: 2814
Filing Date: January 14, 2004)	
For: EFFICIENT USE OF)	
WAFER AREA WITH)	
DEVICE UNDER THE PAD)	
APPROACH)	

RESPONSE TO OFFICE ACTION
(Election without Traverse)

Hon. Assistant Commissioner for Patents
Alexandria, VA 22313-1450

Sir/Madam:

In the Office Action mailed on April 4, 2005, the Examiner states that the present Application contains two distinct inventions related as (I.) a process of making, and (II.) a product made. As such, the Examiner is requiring the Applicants to elect a single invention for examination. Specifically, the Examiner is requiring the Applicants to elect between a first Group I, recited in Claims 13-21, relating to a method for fabricating a semiconductor structure, and a second Group II, recited in Claims 1-9 and relating to a semiconductor structure, and Claims 10-12 and relating to a pad area apparatus for a semiconductor structure. Responsively, Applicants respectfully submit herein an election without traverse and a listing of the Claims, beginning on page 2 of this paper.